



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/834,660
Filing Date April 12, 2001
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Laura Schillinger
Attorney's Docket No. MI22-1637
Title: Semiconductor Processing Methods Of Forming Transistors, Semiconductor
Processing Methods Of Forming Dynamic Random Access Memory
Circuitry, and Related Integrated Circuitry

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT


References -See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

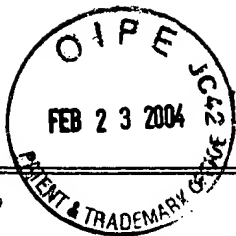
Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: Sept. 30, 2002

By: 
Frederick M. Fliegel, Ph.D.
Reg. No. 36,138

EL979954655



Sheet 1 of 3

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
MI22-1637SERIAL NO.
09/834.660LIST OF ART CITED BY APPLICANT
(Use several sheets if necessary)APPLICANT
Luan C. TranFILING DATE
April 12, 2001GROUP
2813

U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,926,723	07/20/99	Wang			
	AB	6,420,749 B1	07/16/02	Divakaruni et al.			
	AC	6,177,324 B1	01/23/01	Song et al.			
	AD	6,429,081 B1	08/06/02	Doong et al.			
	AE	6,121,651	09/19/00	Furukawa et al.			
	AF	5,492,858	02/20/96	Bose et al.			
	AG	6,323,082 B1	11/27/01	Furukawa et al.			
	AH	6,156,620	12/05/00	Puchner et al.			
	AI	6,048,765	04/11/00	Wu			
	AJ	6,351,019 B1	02/26/02	DeBrosse et al.			
	AK	6,153,467	11/28/00	Wu			

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

	AQ		Wolf et al., "Silicon Processing for the VLSI Era" - Volume 1 - Process Technology - Second Edition, © 2000, 6 pages.
	AR		
	AS		

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1637		SERIAL NO. 09:834,660	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Luan C. Tran			
				FILING DATE April 12, 2001		GROUP 2813	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,236,079 B1	05/22/01	Nitayama et al.			
	AB	6,294,422 B1	09/25/01	Sunouchi et al.			
	AC	6,133,055	10/17/00	Yeh			
	AD	6,353,242 B1	03/05/02	Watanabe et al.			
	AE	6,037,671	03/14/00	Kepler et al.			
	AF	6,028,784	02/22/00	Mori et al.			
	AG	6,025,623	02/15/00	Sunouchi et al.			
	AH	6,261,883 B1	07/17/01	Koubuchi et al.			
	AI	6,403,484 B1	06/11/02	Lim et al.			
	AJ	5,994,198	11/30/99	Hsu et al.			
	AK	6,060,394	05/09/00	Wu			
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation Yes No
	AL						
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
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U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,312,997 B1	11/06/01	Tran			
	AB	6,411,555 B1	06/25/02	Tran			
	AC	5,892,707	04/06/99	Noble			
	AD	6,171,929 B1	01/09/01	Yang et al.			
	AE	6,391,756 B1	05/21/02	Pan et al.			
	AF	6,423,620 B2	07/23/02	Pan et al.			
	AG	6,297,129 B2	10/02/01	Tran et al.			
	AH	5,447,884	09/05/95	Fahey et al.			
	AI	5,851,900	12/22/98	Chu et al.			
	AJ						
	AK						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
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